

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10002217
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01207M	

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/JP/	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
/JP/	2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm ; 2 pages printed on 4/12/2005	<input type="checkbox"/>
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/JP/	12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	<input type="checkbox"/>
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/JP/	23	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2006 11/8/2007 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages	<input type="checkbox"/>
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/JP/	34	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 250 pages 264 pages	<input type="checkbox"/>
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